

**FIG. 1**  
(PRIOR ART)

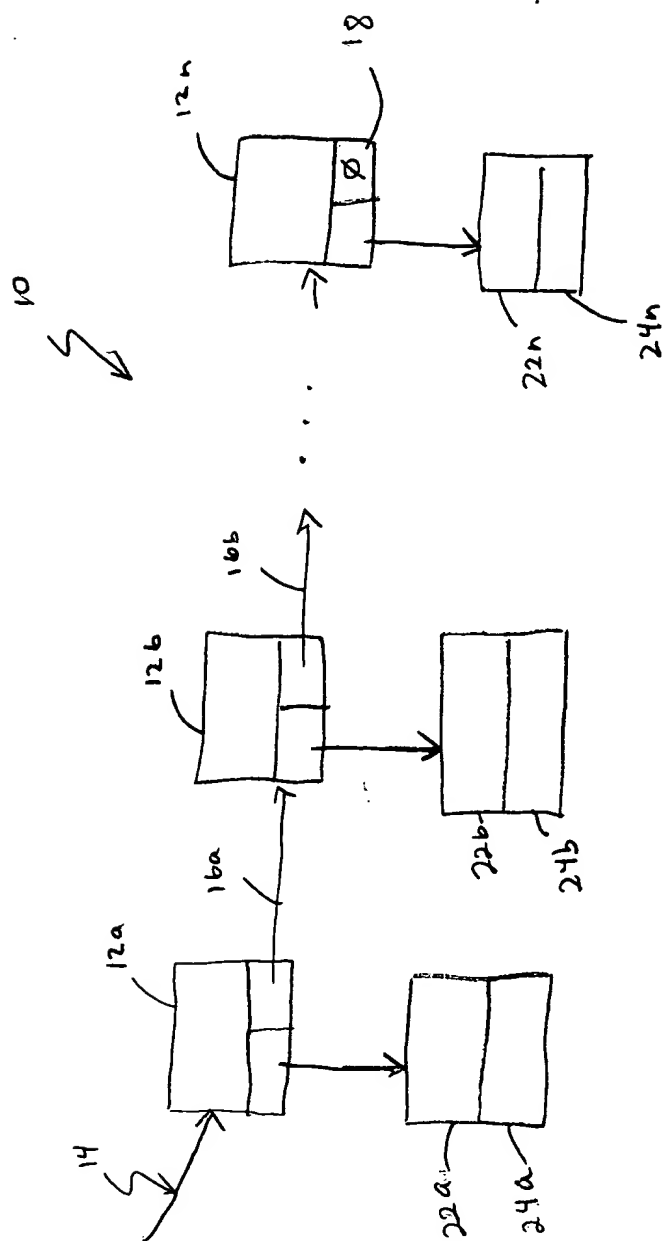


Fig. 2

FIG. 3 is a block diagram of a unit architecture 26, which includes a TNF kernel 38, three link drivers 40a, 40b, and 40c, three links 28a, 28b, and 28c, and three PHYs 32a, 32b, and 32c. The unit architecture 26 is connected to a network 34.

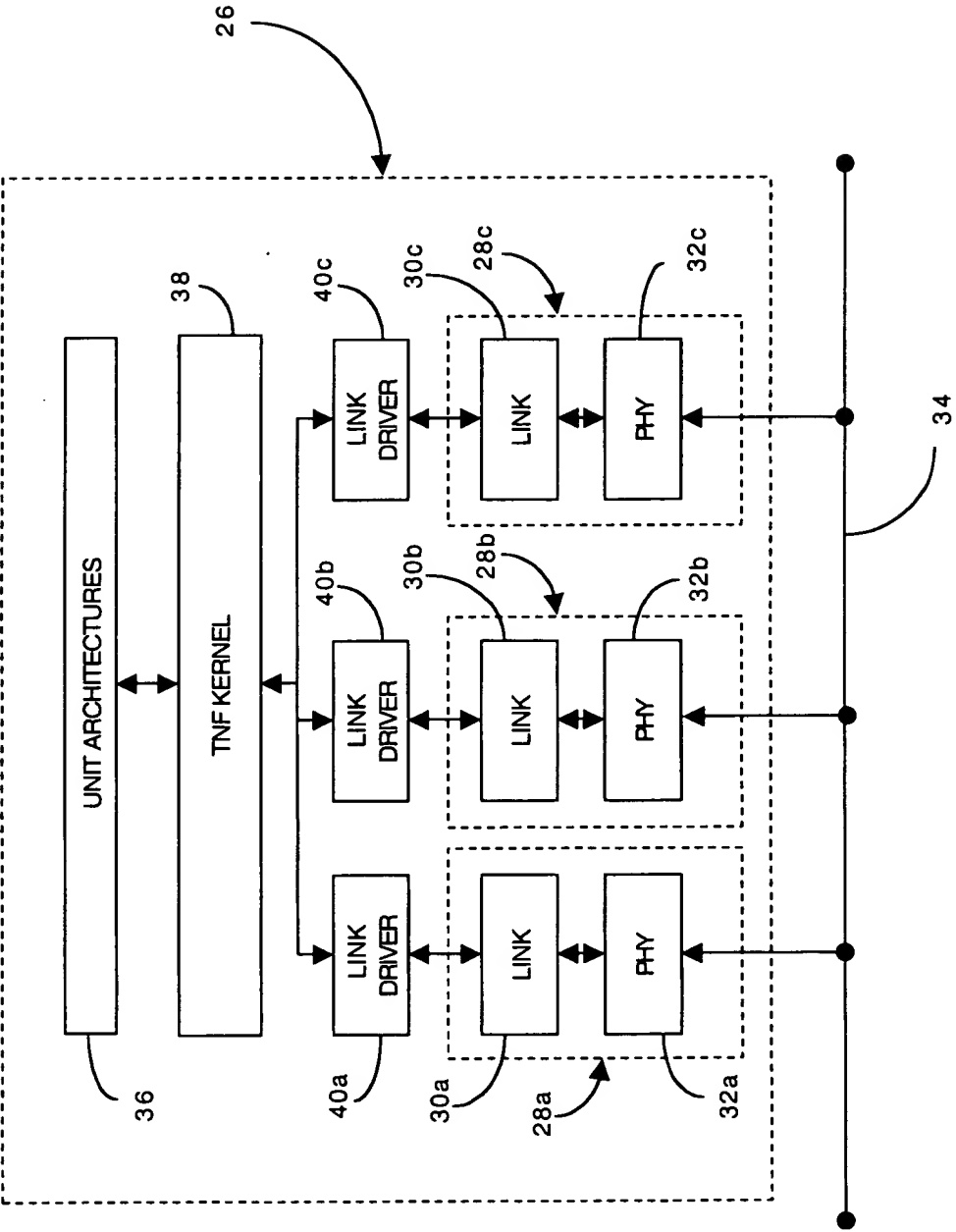


FIG. 3

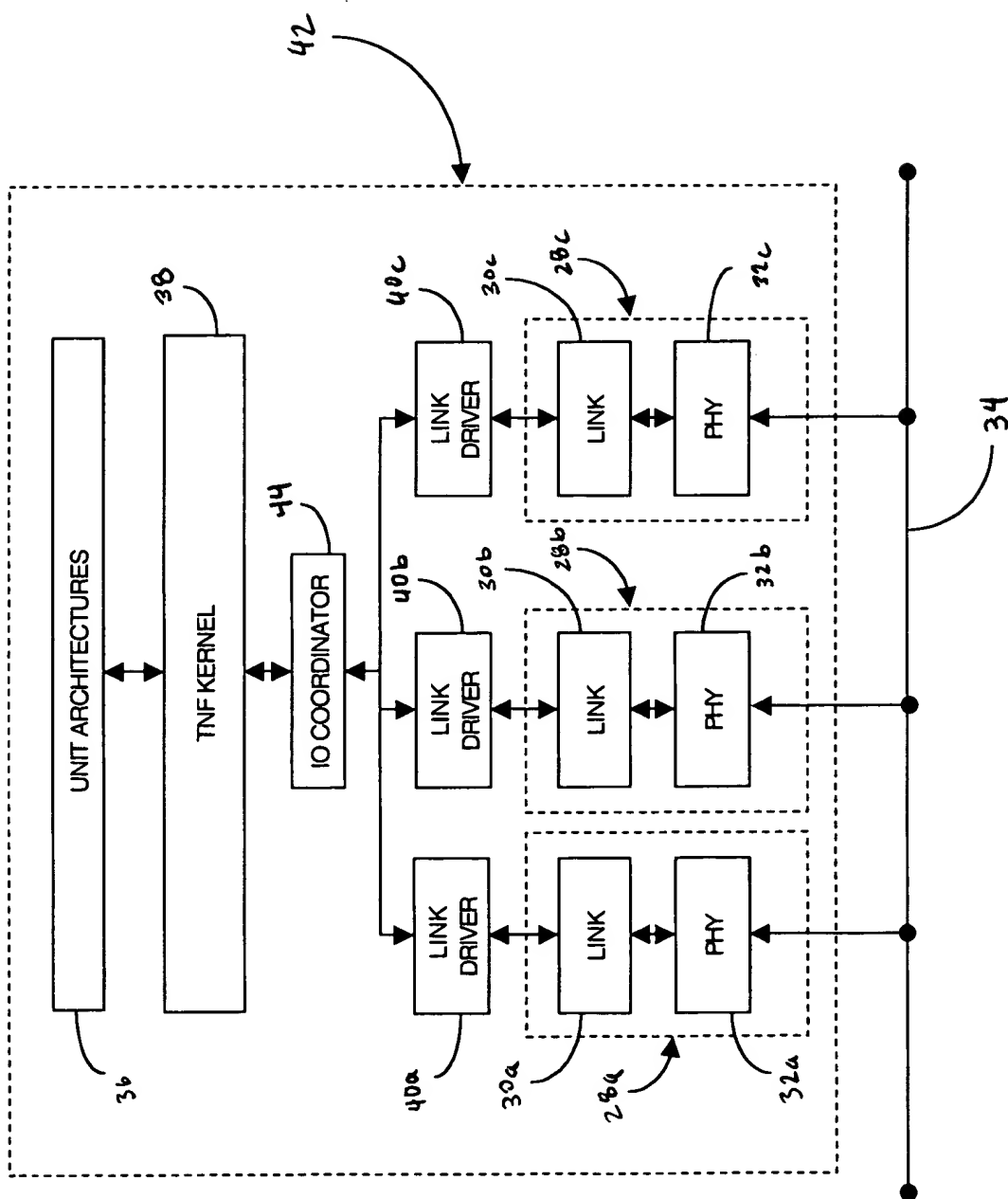


FIG. 4

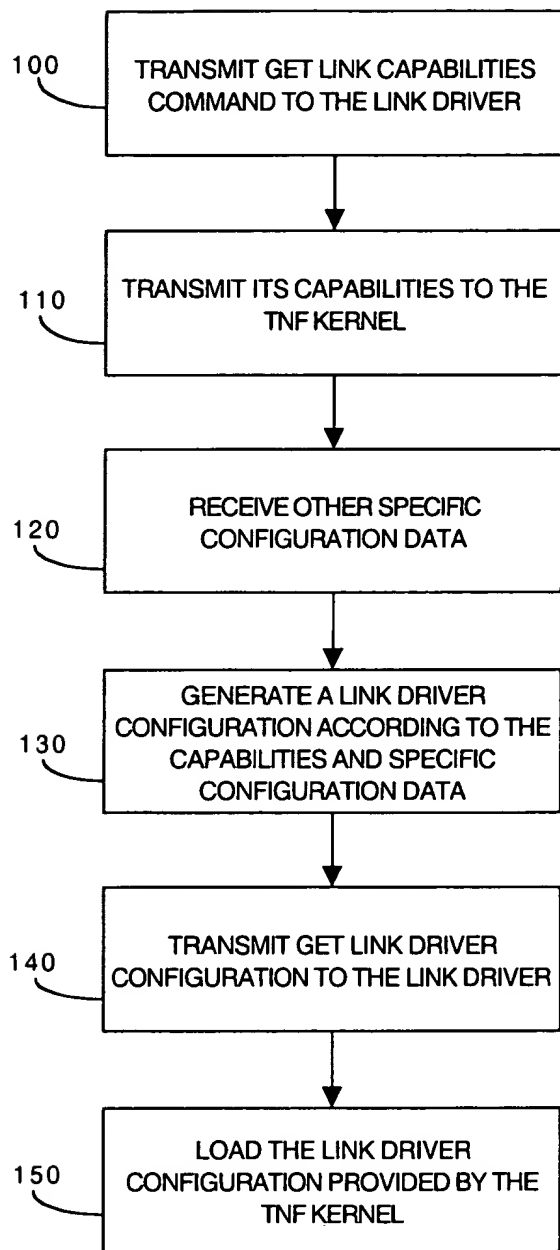


FIG. 5